



AP/IRW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroyuki NANSEI et al.**

Group Art Unit: **2822**

Serial No.: **10/643,967**

Examiner: **Toniae M. THOMAS**

Filed: **August 20, 2003**

P.T.O. Confirmation No.: **4992**

For: **SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE**

Attorney Docket No.: **030993**

Customer No.: **38834**

**AMENDMENT AFTER FINAL UNDER 37 CFR § 1.116**

**Mail Stop AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

February 17, 2005

Sir:

In response to the Office Action dated December 2, 2004, Applicants amend the above-identified application as follows and submit the following remarks.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.